

## Circuiti Integrati Digitali Rabaey Pdf 12 [TOP]

Chapter 1 The first practical MOS integrated circuits were implemented in. The principle of their operation is based on a peculiar effect, which is associated with the performance of one of the fundamental operations of combining in the node when using non-electric charge carriers. Near the crystal growth zone, the dielectric constant of the semiconductor decreases, and the resistance is caused by the dielectric cushion. This pillow is, in fact, a dielectric screen, so if there are charge carriers in the growth zone in the semiconductor, they will immediately split into positive ones, and positive ones into negative ones. In this case, it turns out that there are two possible directions of current flow through the dielectric layer: when the current is directed from the phase of the positively charged nucleus to the phase of the negatively charged ions, one of the paths in the direction of the current turns out to be the smallest, so the positive ion is directly under the positive charge and is pushed out of the growth zone, while the positive electron of the atom passes into the nucleus. At the same orientation of the growth zone, the orientation opposite to the orientation of the positive growth zone begins to dominate. In the presence of strong electrons in the crystal, most of which have magnetic properties, due to the proximity to the growth zone, a negative ion turns out to be, and the direction of its movement coincides with the direction of the current through the growth zone (it is more correct to say that the direction of movement of the ion is determined by the bending electrode). This phenomenon is called "blocking". Thus, problems arise in transluence electrodes, of which the loss of the property of the dielectric to push out negative ions is most pronounced (for example, in silicon crystals). Because of this, a layer of particles with a very high spin moment appears. In this case, the losses become very large (the phase shift exceeds several orders of magnitude), and the ability of some semiconductors to retain them even after a significant polarization of the ejectors turns out to be impossible. A feature of MOS structures with mixed growth zones is that the ejectors tend to eject all the ions from under the screen until their surface captures all the ejected ions. This circumstance leads to the fact that only some of them are under the screen and some of them remain in the growth area. In this regard, at low intensities of current flow through the diodes, an increase in the degree of their action and power is observed, however, as soon as the flow energy falls below the dielectric loss limit, the power drops sharply. Conversely, in regions with relatively high flow rates (e.g. in diodes), the density of charged ions can be very high

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